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and you can't. A: Ok, as soon as I realised your problem was a Mac OS X compatible one, rather than a problem with ZBrush or his associated. But thanks for the warning, I'll try to reproduce it in a virtual machine for the time being, and try and find a solution. 1. Field of the Invention The present invention relates to the technical field of memory apparatus and system using the memory apparatus and to a memory control method, in particular, a memory control method for recovering a memory apparatus which is idle when a master signal is not input thereto. 2. Description of the Related Art A conventional DDR (Double Data Rate) memory apparatus which processes a double data rate inputted from a memory controller 1 to output the processed data in synchronization with a clock CLK is disclosed in Japanese Patent Application Laid-Open No. H09-215871. When the clock signal is inputted to the memory controller 1 and the memory controller 1 outputs the data to a slave side, the memory apparatus including memory cells functions as a receiver, and in a memory operation, both of the inputting and outputting of the data is enabled. When the clock signal is not inputted, the memory apparatus including memory cells is stopped and does not change from a low-impedance state to a high-impedance state or from a high-impedance state to a low-impedance state. Therefore, even if the data is inputted from a master side, there is no change in a state of the memory apparatus as the memory apparatus does not function as the receiver. Thus, although the clock signal is not inputted, the state of the memory apparatus does not change. As mentioned above, when the clock signal is not inputted for a period, the state of the memory apparatus including the memory cells does not change even if the data is inputted from the master side. Thus, an idle state of the memory apparatus including memory cells continues until the clock signal is received. Therefore, in a conventional memory apparatus, there is a problem in that the amount of current consumption increases in the case of a standby state, and a load on a control circuit increases due to a clock signal of a clock buffer.Q: How to access